SPECIFICATIONS

CUSTOMER . CDE012

SAMPLE CODE . S05D00099-00

MASS PRODUCTION CODE . P05D00099-00

SAMPLE VERSION . 01

SPECIFICATIONS EDITION . 002

DRAWING NO. (Ver.) . LMD-P05D00099-00 (Ver.002)

PACKAGING NO. (Ver.) . PKG-P05D00099-00 (Ver.001)

Customer Approved

Date:

Approved	Checked	Designer
廖志豪	陳宗淇	廖郁鈞
Rex Liao	Howard Chen	Grace Liao
		POWER

□ Preliminary specification for design input

■ Specification for sample approval

POWERTIP TECH. CORP.

Headquarters:

No.8, 6th Road, Taichung Industrial Park,

Taichung, Taiwan

台中市 407 工業區六路 8號

TEL: 886-4-2355-8168

FAX: 886-4-2355-8166

E-mail: sales@powertip.com.tw

Http://www.powertip.com.tw

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History of Version

Date (mm / dd / yyyy)	<u>Ver.</u>	Edi.	<u>Description</u>	<u>Page</u>	Design by
09/05/2023	01	001	Preliminary.	-	Yuan
03/18/2024	01	002	First Sample.		Grace



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1. SPECIFICATIONS

1.1 Mechanical Specifications

<u>Item</u>	Standard Value		
Outline Dimension	85.0(L) * 56.0(W) * 8.5(H)(Max)		
	THIS PRODUCT CONFORMS THE ROHS OF PTC		
ROHS	Detail information please refer website:		
	http://www.powertip.com.tw/news_detail.php?Key=1&cID=1		

1.2 Absolute Maximum Ratings

<u>ltem</u>	<u>Symbol</u>	<u>Condition</u>	Min.	Max.	<u>Unit</u>	Remark
Power Supply for Digital Circuit	VDC	GND=0V	-0.3	5.25	V	
Power Supply for TFT Panel	VDD	GND=0V	-0.3	4.5	V	
Power Supply for Backlight Unit	VCC	GND=0V	-0.3	+15.0	V	-
Operating Temperature	Top (Ts)	Note 1	-20	70	°C	
Storage Temperature	T _{ST} (Ta)	Note 2	-30	80	°C	
Storage Humidity	H₀	Ta < 60 °C	-	90	RH	

The absolute maximum rating values of this product are not allowed to be exceeded at any time. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

Note 1: Ts is the temperature of panel's surface

Note 2: Ta is the ambient temperature of samples



1.3 DC Electrical Characteristics

<u>ltem</u>	Symbol	<u>Condition</u>	Min.	<u>Typ.</u>	Max.	<u>Unit</u>
Power Supply for Digital Circuit	VDC	GND=0V	4.75	5	5.25	V
Power Supply for TFT Panel	VDD	GND=0V	3.0	3.3	3.6	V
Power Supply for Backlight Unit	VCC	GND=0V	5	12	15	V
Input Voltage for	VIH	GND=0V	0.7VDD	-	VDD	V
TFT Panel	VIL	GND=0V	0	-	0.3VDD	V
Supply Current	IDC	@VDC=5V	-	1	1.5	A
Input Voltage for	VPH	GND=0V	1.2	_	-	V
PWM Signal	VPL	GND=0V	-	-	0.4	V
Dimming Clock Rate	fP	GND=0V	5	-	100	KHz

Note: Used SH800480T013-IHC27 to test object.





1.4 Firmware Description

1.4.1 PWM Set Protocol

Instruction structure

I2C address + Command + Data

I²C Address

Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
1	1	1	1	0	0	0	R/W

Bit 0: 0 for Write / 1 for Read

Command and Data

Command	<u>Data</u>	<u>Description</u>
0xC2	PWM Set number	PWM set 8bits data(0~0xff)

1.4.2 Firmware Information

EDID Firmware Information

File: PH800480T_all.bin

SHA-256: 8485d69bd4a347a097c9f3a0f65c20cd5ed21d94d57c9dff9364d26da4e6bc3e

Remark:

Support Display Resolution 800*480

MCU Firmware Information

File: USB_CTP_PWM_ALL.hex

SHA-256: 7cb21861ca946d01811f97140318cd302c6d257c22e6017424bea41f7a7ca9dc

Remark:

Support Display Resolution 800*480



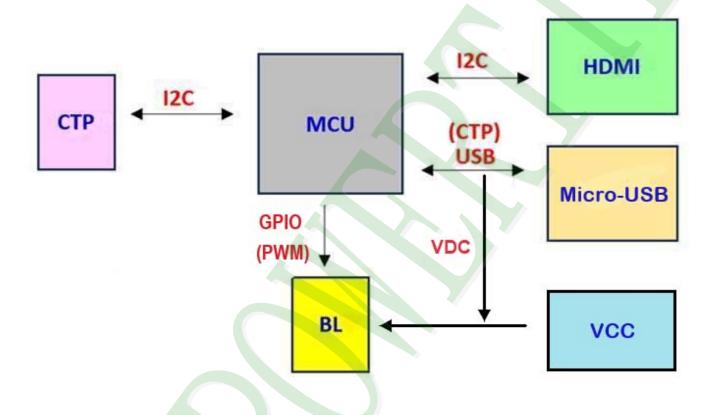
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 Mechanical Diagram

* See Appendix

2.1.2 Block Diagram





2.2 Interface Pin Description

2.2.1 J1(HDMI 1.3 A type Interface)

<u>Pin No.</u>	<u>Symbol</u>	<u>Function</u>
1	TX2+	TMDS Data 2+
2	TX2 Shield	TMDS Data 2 Shield
3	TX2-	TMDS Data 2-
4	TX1+	TMDS Data 1+
5	TX1 Shield	TMDS Data 1 Shield
6	TX1-	TMDS Data 1-
7	TX0+	TMDS Data 0+
8	TX0 Shield	TMDS Data 0 Shield
9	TX0-	TMDS Data 0-
10	TXC+	TMDS Clock+
11	TXC Shield	TMDS Clock Shield
12	TXC-	TMDS Clock-
13	CEC	CEC
14	NC	No Connection
15	SCL	Serial Clock for DDC
16	SDA	Serial Data for DDC
17	GND	Power Ground
18	V5V	+5V Power
19	Hot Plug Detect	Hot Plug Detect

2.2.2 PC1(Power Supply Input)

Pin No.	Symbol	<u>Function</u>
1	VCC	External Power Supply Voltage: 5V~15V
2	GND	Power Ground.



2.2.3 J2(Micro USB)

Pin No.	<u>Symbol</u>		<u>Function</u>
1	VDC	USB Power	
2	D-	Data-	
3	D+	Data+	
4	ID	No Connection	
5	GND	Power Ground.	

2.2.4 J4(Capacitive Touch Panel Interface)

Pin No.	<u>Symbol</u>	<u>Function</u>
1	GND	Power Ground.
2	TPVDD	Power Supply Voltage (3.3V)
3	SCL	I2C Clock
4	SDA	I2C Data
5	INT	Active Low
6	RST	Active low global reset signal input.

2.2.5 J3(LCM Interface)

Pin#	<u>Name</u>	<u>DESCRIPTION</u>		
1	GND	Power Ground.		
2	VDD	Power for Digital Circuit.		
3	VDD	Power for Digital Circuit.		
4	VCC	Power For LED backlight.		
5	VCC	Power For LED backlight.		
6	PWM	Shutdown & Dimming control input for backlight. Do not allow this pin to float. "Hi" =100%, "Low" = 0%.		
7	GND	Power ground.		
8	R0	Red Data.		
9	R1	Red Data.		
10	R2	Red Data.		



Pin#	<u>Name</u>	DESCRIPTION
11	R3	Red Data.
12	GND	Power Ground.
13	R4	Red Data.
14	R5	Red Data.
15	R6	Red Data.
16	R7	Red Data.
17	GND	Power Ground.
18	G0	Green Data.
19	G1	Green Data.
20	G2	Green Data.
21	G3	Green Data.
22	GND	Power Ground.
23	G4	Green Data.
24	G5	Green Data.
25	G6	Green Data.
26	G7	Green Data.
27	GND	Power Ground.
28	В0	Blue Data.
29	B1	Blue Data.
30	B2	Blue Data.
31	B3	Blue Data.
32	GND	Power Ground.
33	B4	Blue Data.
34	B5	Blue Data.
35	B6	Blue Data.
36	B7	Blue Data.
37	GND	Power Ground.
38	HS	Line synchronization signal. Horizontal Sync Input.
39	VS	Frame synchronization signal. Vertical Sync Input.
40	GND	Power ground.

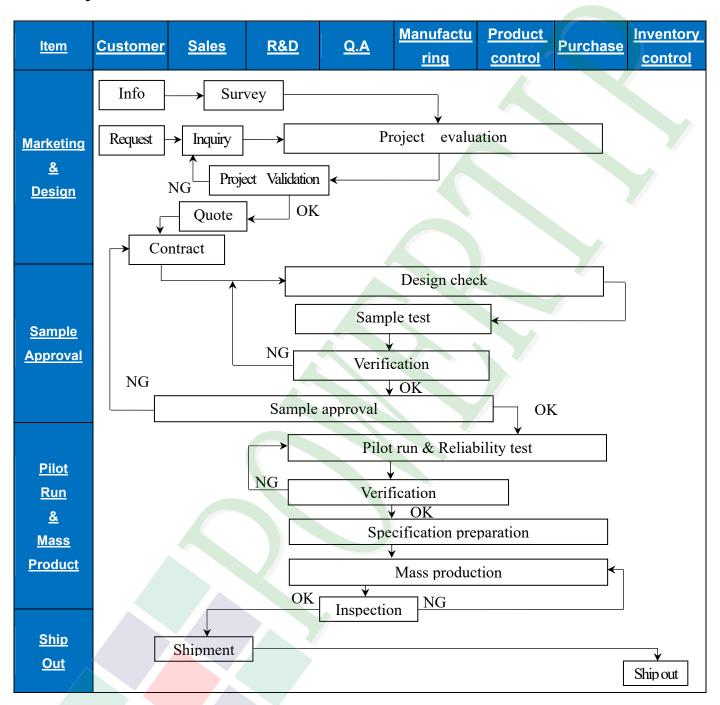


Pin#	<u>Name</u>	<u>DESCRIPTION</u>		
41	DE	Display enable pin from controller. Data Input Enable.		
42	GND	Power Ground.		
43	DCLK	Sample clock. Data will be latched at the falling edge of DCLK.		
44	GND	Power Ground.		
45	CS(NC) / ID1	No Function./ ID[4:1]These pins select LCM type.		
46	SDIN(NC) / ID2	No Function./ ID[4:1]These pins select LCM type.		
47	SCK(NC) / ID3	No Function ./ ID[4:1]These pins select LCM type.		
48	DISPLAY CONTROL / ID4	Display Enable(Hi Active)./ ID[4:1]These pins select LCM type.		
49	/RESET	Global Reset (Low Active).		
50	GND	Power Ground.		

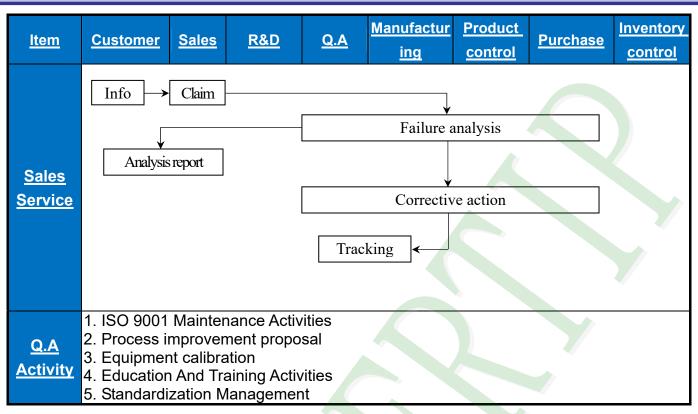


3. Quality Assurance System

3.1 Quality Assurance Flow Chart









3.2 Inspection Specification

◆Scope: The document shall be applied to TFT-LCD Module for 3.5"-15″ (Ver.B01).

◆Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.

◆Equipment: Gauge, MIL-STD, Powertip Tester, Sample

◆Defect Level: Major Defect AQL: 0.4; Minor Defect AQL: 1.5

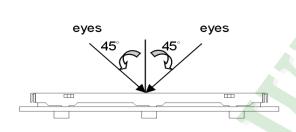
♦OUT Going Defect Level: Sampling

◆Standard of the product appearance test:

a. Manner of appearance test:

(1). The test best be under 20W×2 fluorescent light(about 300lux \sim 500lux) and distance of view must be at 30~40 cm.

(2). The test direction is base on about around 45° of vertical line.



5% Brightness

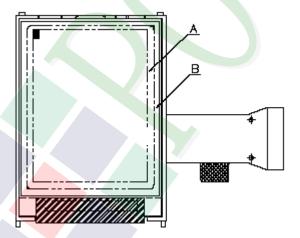
ND fliter

30~40 cm

LCD panel

2.5~3cm

(3). Definition of area.



A area: viewing

B area: Outside of viewing area

(4). Standard of inspection: (Unit: mm)



♦Specification For TFT-LCD Module 3.5"~15":

NO	<u>Item</u>	<u>Criterion</u>				
	Product condition	1.1 The part number is inconsistent with work order of production.				
01		1.2 Mixed product types.				
		1.3 Assembled in inverse direction.	Major			
02	Quantity	2.1 The quantity is inconsistent with work order of production.	Major			
03	Outline dimension	3.1 Product dimension and structure must conform to structure diagram.	Major			
		4.1 Missing line character and icon.	Major			
		4.2 No function or no display.	Major			
		4.3 Display malfunction.	Major			
04	Electrical Testing	4.4 LCD viewing angle defect.				
		4.5 Current consumption exceeds product specifications.				
		4.6 Mura cannot be seen through 5% ND filter at 50% Gray, should be judged by the viewing angle of 90 degree.	Minor			
	Dot defect	$\frac{\text{Item}}{\text{Bright Dot}} \frac{\text{Acceptance (Q'ty)}}{\leq 4}$				
		Dark Dat < 5				
		$\begin{array}{c cccc} Dot & Dark Dot & \leq 3 \\ \hline Defect & Joint Dot & \leq 3 \end{array}$				
		Total ≤ 7				
	(Bright dot,	5.1 Inspection pattern: full white, full black, Red, Green				
05	Dark dot)	and blue screens.				
	On -display	5.2 It is defined as dot defect if defect area > 1/2 dot.				
	On -display	5.3 The distance between two dot defect ≥5 mm.				
		5.4 Bright dot : Dots appear bright and unchanged in visible with 5% ND filter is defined.				
		5.5 Tiny bright dot: bright dot area ≤1/2 dot.				
		a. Dots appear bright and unchanged in visible with 5% ND filter				
		is defined defect and is judged in accordance with 6.1				
		b. Dots invisible with 5% ND Filter is Ignored.				



♦ Specification For TFT-LCD Module 3.5" ~15":

NO NO	<u>Item</u>	<u>Criterion</u>				Level				
		6.1 R	ound type	(Non-displ	ay or d	lisplay):				
								(0)		
			Dimension	n (diameter	<u>: Ф)</u>	Accepts A area	ance	(Q'ty) B are	2	
				Ф ≦ 0.	25	Ignore		Dare		
	Black or white		0.25 <	< Φ ≦ 0 .		5				
	Dot, scratch,			Φ > 0.	50	0		Ignor	е	
	contamination			Total		5				
	Round type	6.2 L	ine type(No	on-display	or disp	olay):				·
	$X \leftarrow X$	m	odule size	Length	W	idth (W)			ee (Q'ty)	
06	Y Y		<u> </u>	<u>(L)</u>		W ≤ 0.03		area	B area	Minor
00	$\Phi = (x+y)/2$			L ≦10.0	0.03	$\frac{VV \leq 0.03}{\langle V \leq 0.05}$		nore 4		IVIIIIOI
	(2 1 3) / =			L ≦5.0		<w 0.10<="" td="" ≤=""><td></td><td>2</td><td></td><td></td></w>		2		
		<u>3.</u>	<u>5" to less</u> <u>9"</u>					As	Ignore	
	Line type		<u>7</u>			W > 0.10		und		
	↓				Total			γpe 5		
	✓ † W				13.13.1	W ≤ 0.05		nore		
	→ı _т			L ≦10.0	0.05	<w 0.10<="" td="" ≦=""><td></td><td>5</td><td></td><td></td></w>		5		
	L	q	o" to 15"					As	Ignore	
		2	10 13	-		W > 0.10		und ⁄pe	Ignoro	
					Total			рс 5		
						•				
		Di	mension (diameter:	Ф)	Accep	tanc	e (Q'ty	<u>/)</u>	
					Ψ)_	A area		<u>B</u>	<u>area</u>	
07	Polarizer			⊅ ≤ 0.25		Ignore				Minor
07	Bubble			⊅ ≦ 0.50		4				Minor
				⊅ ≦ 0.80		1		lg	nore	
				Φ >0.80		0				
			To	tal		5				



◆Specification For TFT-LCD Module 3.5" ~15":

NO	<u>Item</u>	<u>Criterion</u>			
		Symbols: X: The length of crack Z: The thickness of crack T: The thickness of glass X: The width of crack W: terminal length a: LCD side length			
		8.1 General glass chip: 8.1.1 Chip on panel surface and crack between panels:			
08	The crack of glass	SP SP SP [NG]	Minor		
		Seal width Z			
		<u>X</u> <u>Y</u> <u>Z</u>			
		≤ a Crack can't enter viewing area ≤1/2 t			
		\leq a Crack can't exceed the half of SP width. 1/2 t $<$ Z \leq 2 t			



◆Specification For TFT-LCD Module 3.5" ~15":

<u>NO</u>	<u> Item</u>	<u>Criterion</u>			
		X: The length of crack Z: The thickness of crack t: The thickness of glass 8.1.2 Corner crack:			
		<u>X</u> <u>Y</u> <u>Z</u>			
		≤1/5 a Crack can't enter viewing area Z ≤ 1/2 t			
		\leq 1/5 a Crack can't exceed the half of SP width. 1/2 t $<$ Z \leq 2 t			
			Minor		
08	The crack of glass 8.2 Protrusion over terminal: 8.2.1 Chip on electrode pad: X Y X W Y W Y				
		X			
		$\begin{array}{c cccc} X & \underline{Y} & \underline{Z} \\ \hline \text{Front} & \leq \mathbf{a} & \leq 1/2 \mathbf{W} & \leq \mathbf{t} \\ \hline \text{Rock} & \leq \mathbf{a} & \leq \mathbf{W} & \leq 1/2 \mathbf{t} \\ \hline \end{array}$			



♦Specification For TFT-LCD Module 3.5″ ~15″:

NO_	<u> Item</u>	<u>Criterion</u> <u>L</u>				
08	The crack of glass	Symbols: X: The length of crack Z: The thickness of crack T: The thickness of glass A: LCD side length 8.2.2 Non-conductive portion: $ \frac{X}{X} \qquad \frac{Y}{X} \qquad \frac{Z}{X} $ If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications. 8.2.3 Glass remain: $ \frac{X}{X} \qquad \frac{Y}{X} \qquad \frac{Z}{X} $ $ \frac{X}{X} \qquad \frac{Z}{X} $	Level			



♦Specification For TFT-LCD Module 3.5″~15″:

NO	<u>Item</u>	<u>Criterion</u>		
09	Backlight elements	9.1 Backlight can't work normally.		
		9.2 Backlight doesn't light or color is wrong.	Major	
		9.3 Illumination source flickers when lit.	Major	
	General appearance	10.1 Pin type, quantity, dimension must match type in structure diagram.	Major	
		10.2 No short circuits in components on PCB or FPC.	Major	
		10.3 Parts on PCB or FPC must be: no wrong parts, missing parts or excess parts.	Major	
10		10.4 Product packaging must the same as specified on packaging specification sheet.	Minor	
		10.5 The folding and peeled off in polarizer are not acceptable.	Minor	
		10.6 The PCB or FPC between B/L assembled distance(PCB or FPC) is $~\leq 1.5$ mm.	Minor	



4. Reliability Test

4.1 Reliability Test Condition

(Ver.B01)

	r renability rest condition						
NO.	TEST ITEM	TEST CONDITION					
1	High Temperature Storage Test	Keep in 80 ±5°C 240 hrs					
2	High Temperature Operating Test	Keep in 70 ±5°C 240 hrs					
3	Low Temperature Storage Test	Keep in -30 ±5°C 240 hrs					
4	Low Temperature Operating Test	Keep in -20 ±5°C 240 hrs					
5	High Temperature / High Humidity Storage Test	Keep in 60 °C / 90% R.H duratio (Excluding the polarizer)	n for 240 hrs				
6	Temperature Cycling Storage Test	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$					
7	ESD Test	Air Discharge: Apply 2 KV with 5 times Discharge for each polarity +/- 1. Temperature ambiance: 15°C ~35°C 2. Humidity relative: 30% ~60% 3. Energy Storage Capacitance(Cs+Cd): 150pF±10% 4. Discharge Resistance(Rd): 330Ω±10% 5. Discharge, mode of operation: Single Discharge (time between successive discharges at least 1 sec) (Tolerance if the output voltage indication: ±5%)					
8	Vibration Test (Packaged)	 Sine wave 10~55 Hz frequency (1 min/sweep) The amplitude of vibration: 1.5 mm Each direction (X, Y, Z) duration for 2 hrs 					
9	Drop Test (Package <mark>d)</mark>	Packing Weight (Kg 0 ~ 45.4 45.4 ~ 90.8 90.8 ~ 454 Over 454	122 76 61 46				
		Drop Direction : 1 corner / 3 edg	ges / 6 sides each 1time				

○Result Evaluation Criteria :

Under the display quality test conditions with normal operations with normal operation state.

Do not change these conditions as such changes may affect practical display function.

(Normal operation state) Temperature: +20~30°C

Humidity: 50~70%

Atmospheric pressure: 86~106Kpa



5. Precaution Relating Product Handling

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

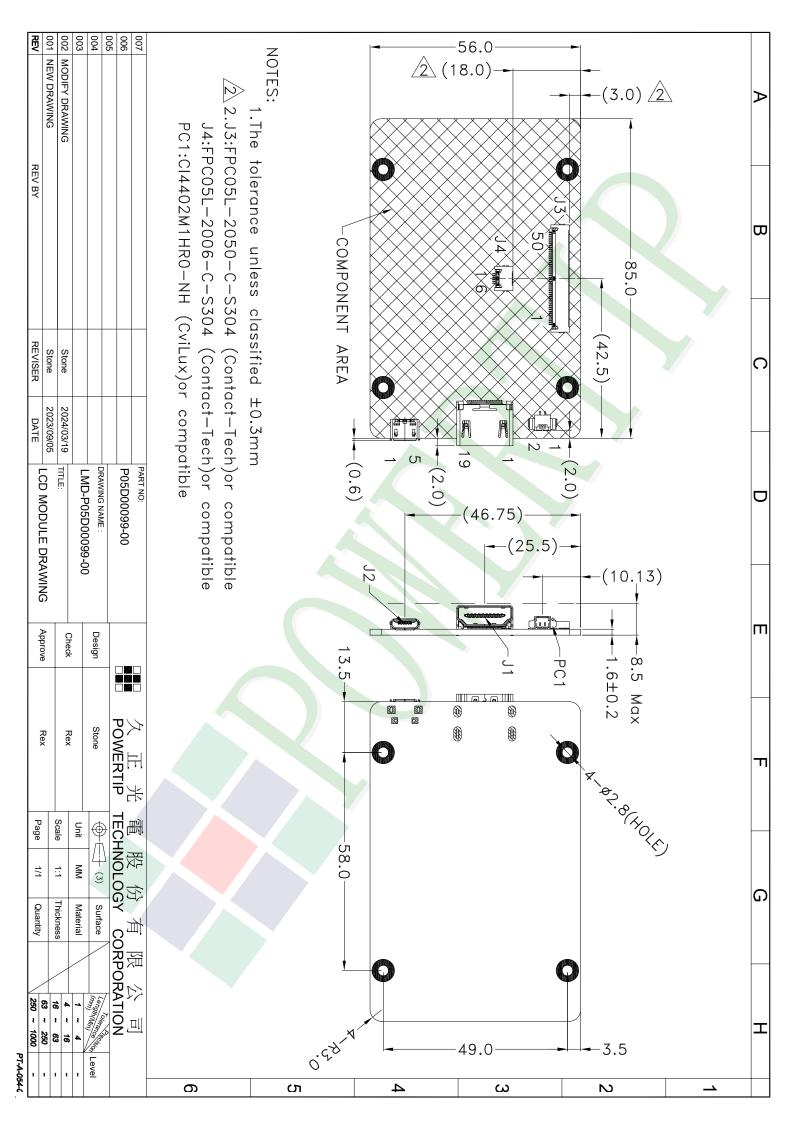
- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully, do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers, etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonic solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320 \pm 10^{\circ}$ C and $3 \sim 5$ sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM.
- 5.2.10 Caution! (LCM products with Capacitive Touch Panel)
 Strong EMI-sources such as switch-mode power supplies (SPS) can lead to touch malfunction (e.g., ghost-touches). Therefore, the touch needs to be thoroughly tested inside the target application.
- 5.2.11 CAUTION: Continuously displaying same static image will result in high possibility of image sticking/image burn-in effect due to TFT panel characteristic.
- 5.2.12 Double-sided tape designed to be attached with the customer's mechanical device, please follow up the rules and regulations published by the original manufacturer of double-side tape for the attachment operation.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is 25°C ± 5°C and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

5.4 TERMS OF WARRANTY

- 5.4.1 Applicable warrant period
 The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility
 - This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



Approve Check Contact Ver.001 Packaging Specifications Rex Rex Stone PKG-P05D00099-00 Documents NO. 1.Packaging Material: (per carton) No. Item Model Dimensions (mm) 1Pcs Weight Quantity Total Weight 1 成品 (LCM) P05D00099-00 85.0 X 56.0 0.0232 192 4.4544 2 抗靜電氣泡袋(1)Bubble Bag BAG000000005 150 X 120 0.002 192 0.384 3 295 X 72 X 3.0 A2-1隔板(2)A2-1 Partition BX29500072BZBA 0.0109 104 1.1336 4 B2-1隔板(3)B2-1 Partition 245 X 72 X 3.0 0.0094 24 0.2256 BX24500072BZBA 5 氣泡紙(4)Bubble Sheet 280 X 240 0.006 0.096 BAG280240BWABA 16 6 C2内盒(5)Product Box 310 X 255 X 86 8 1.28 BX31025580AABA 0.16 7 外紙箱(6)Carton 0.83 0.83 BX52732536CCBA 527 X 325 X 360 1 0.006 8 OTFOAMEP0005BA 0.006 EPE(7)EPE 333 X 218 X 20 1 9 2.一 整箱總重量 (Total LCD Weight in carton): 8.41 Kg±10% 3.單箱數量規格表 (Packaging Specifications and Quantity): (1)Quantity Of Spacer: A2-1隔板 X 13 , B2-1隔板 X 3 (2)Total LCM quantity in carton: quantity per box x no of boxes 192 (4) 氣泡紙 **Bubble Sheet** (1)抗靜電氣泡袋+LCM (7) EPE Bubble Bag+LCM

(4) 氣泡紙 -Bubble Sheet

> (5) C2内盒 Product Box

1. LCM排放示意圖(前後間隔不放置):

模組(LCM) X 1pcs. ■ EPE(7) X 1pcs.

(First and last slot should be empty)

1. LCM placed as figure showing:

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(6)外紙箱 Carton ₩